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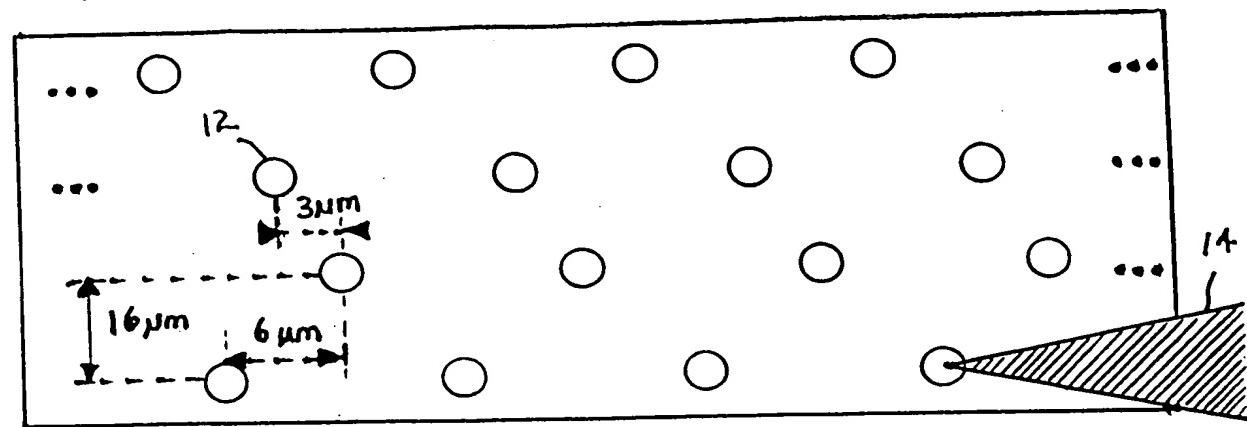


FIGURE 1

FIGURE 2

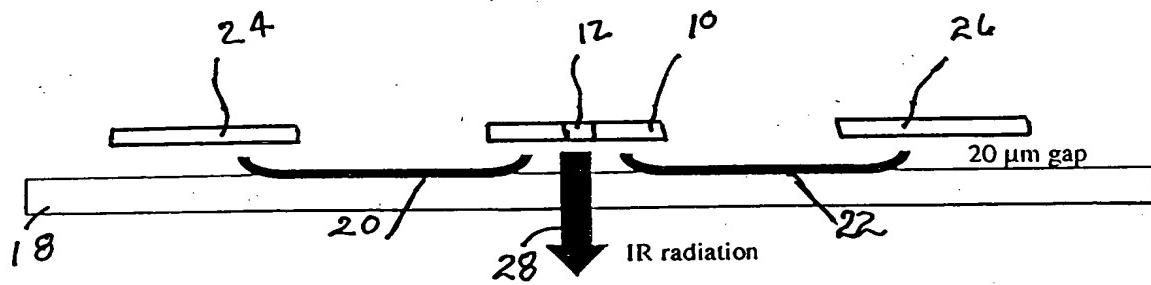


FIGURE 2

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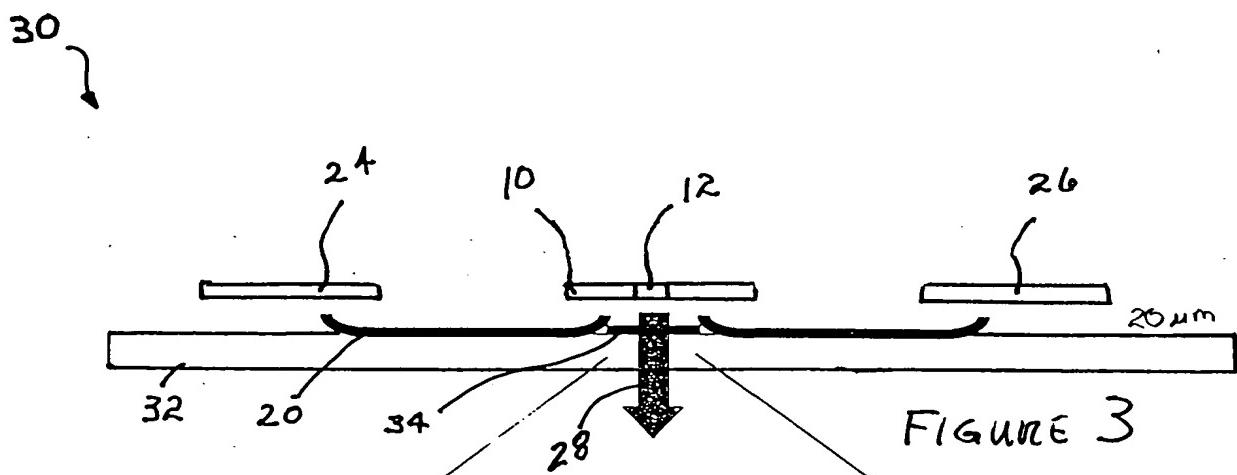


FIGURE 3

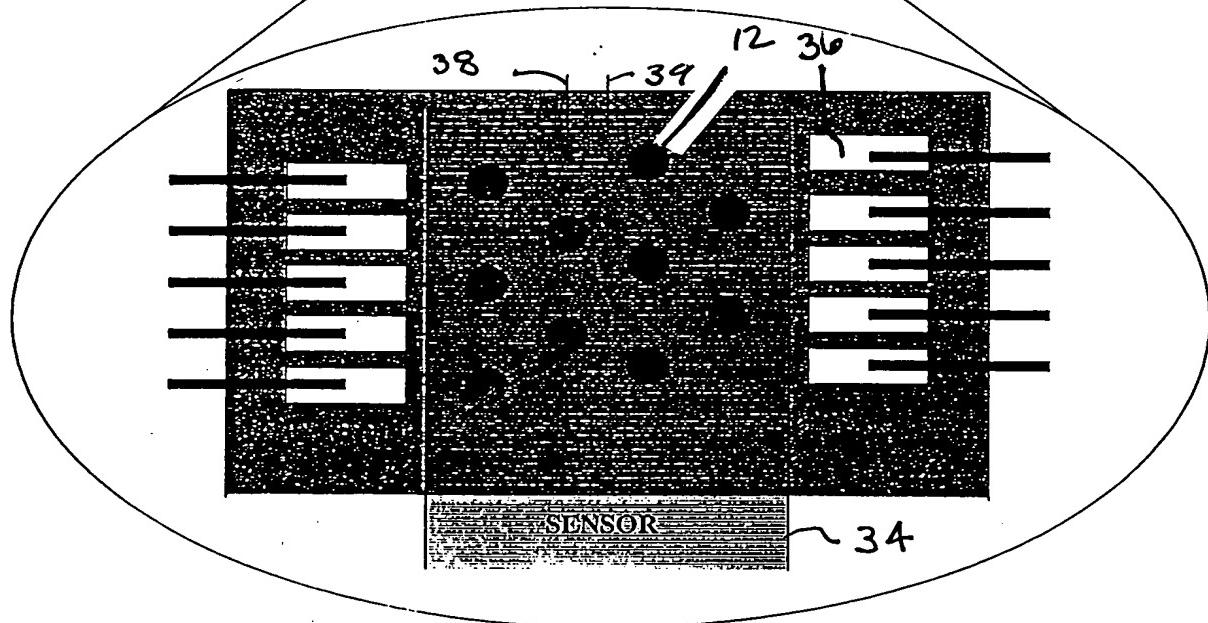


FIGURE 4

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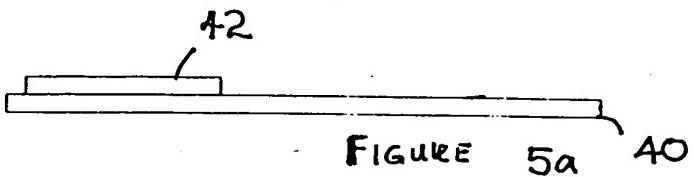


FIGURE 5a

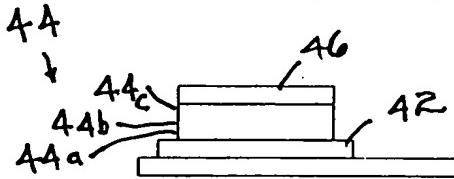


FIGURE 5b

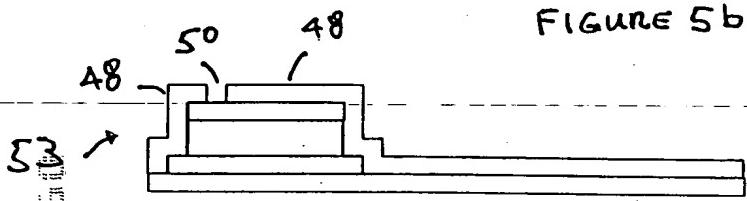


FIGURE 5c

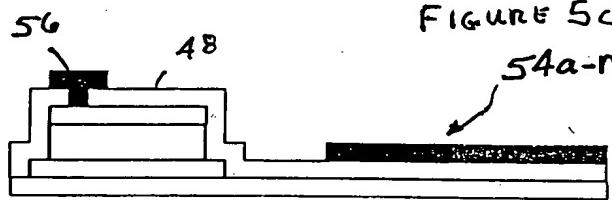


FIGURE 5d

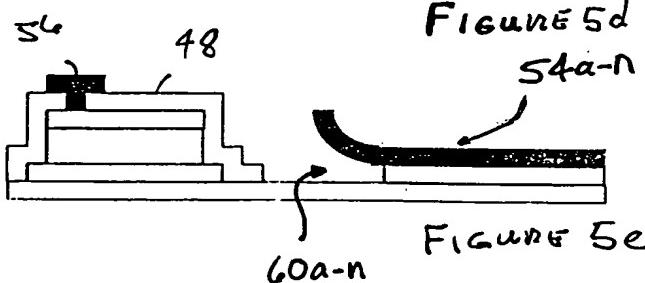


FIGURE 5e

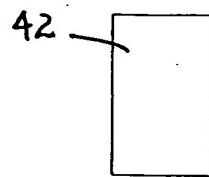


FIGURE 6a

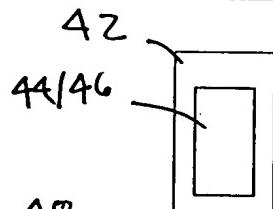


FIGURE 6b

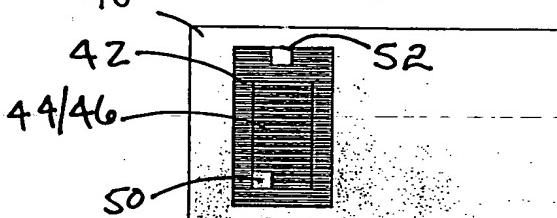
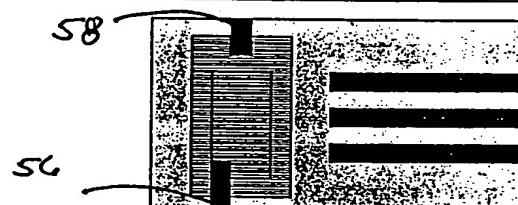
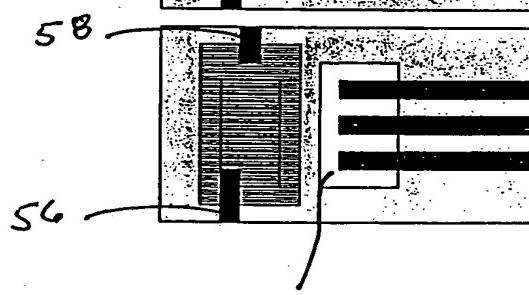


FIGURE 6c



54a-n
60a-n

FIGURE 6d



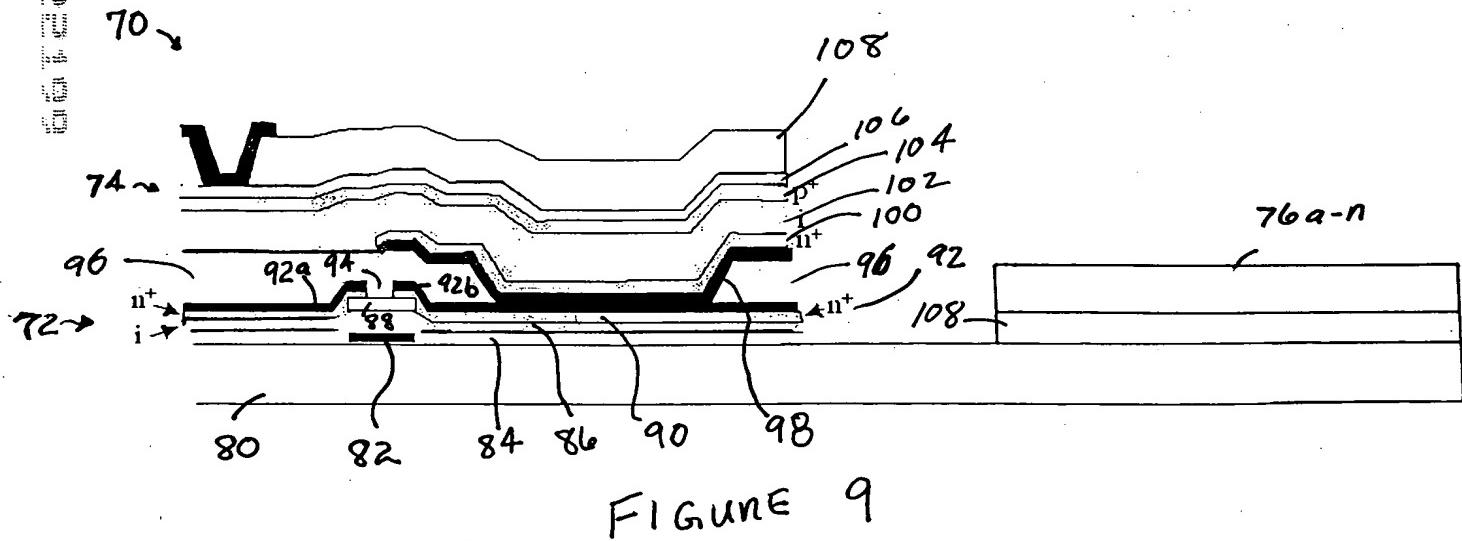
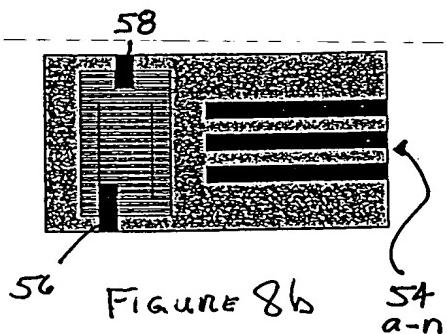
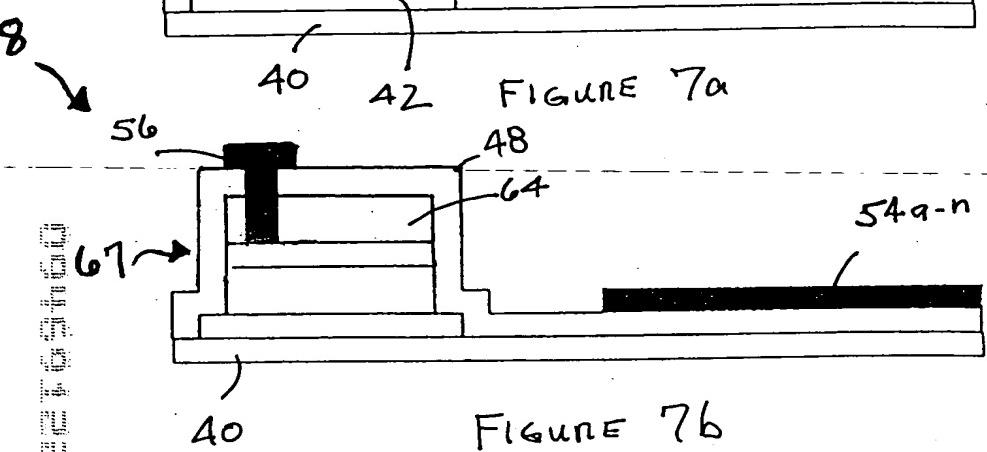
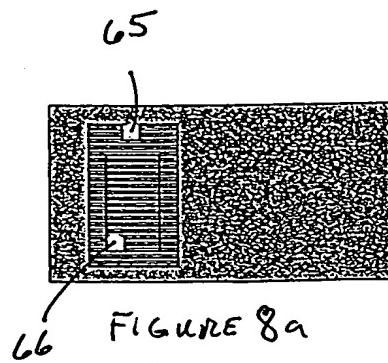
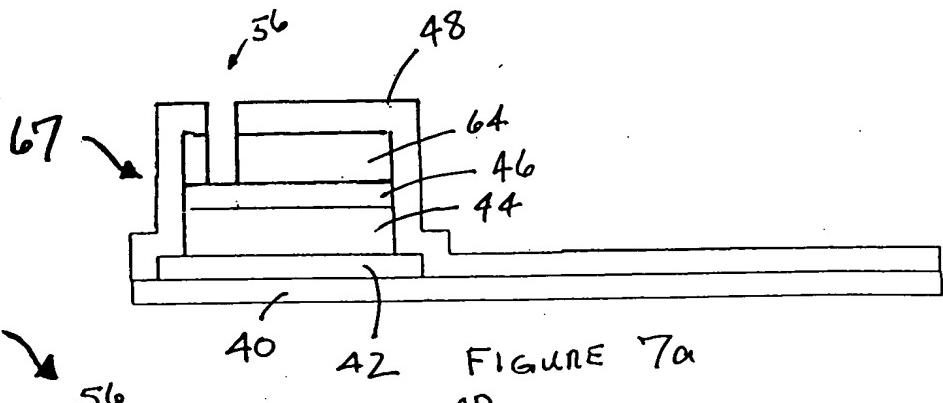
54a-n
60a-n

FIGURE 6e

60a-n

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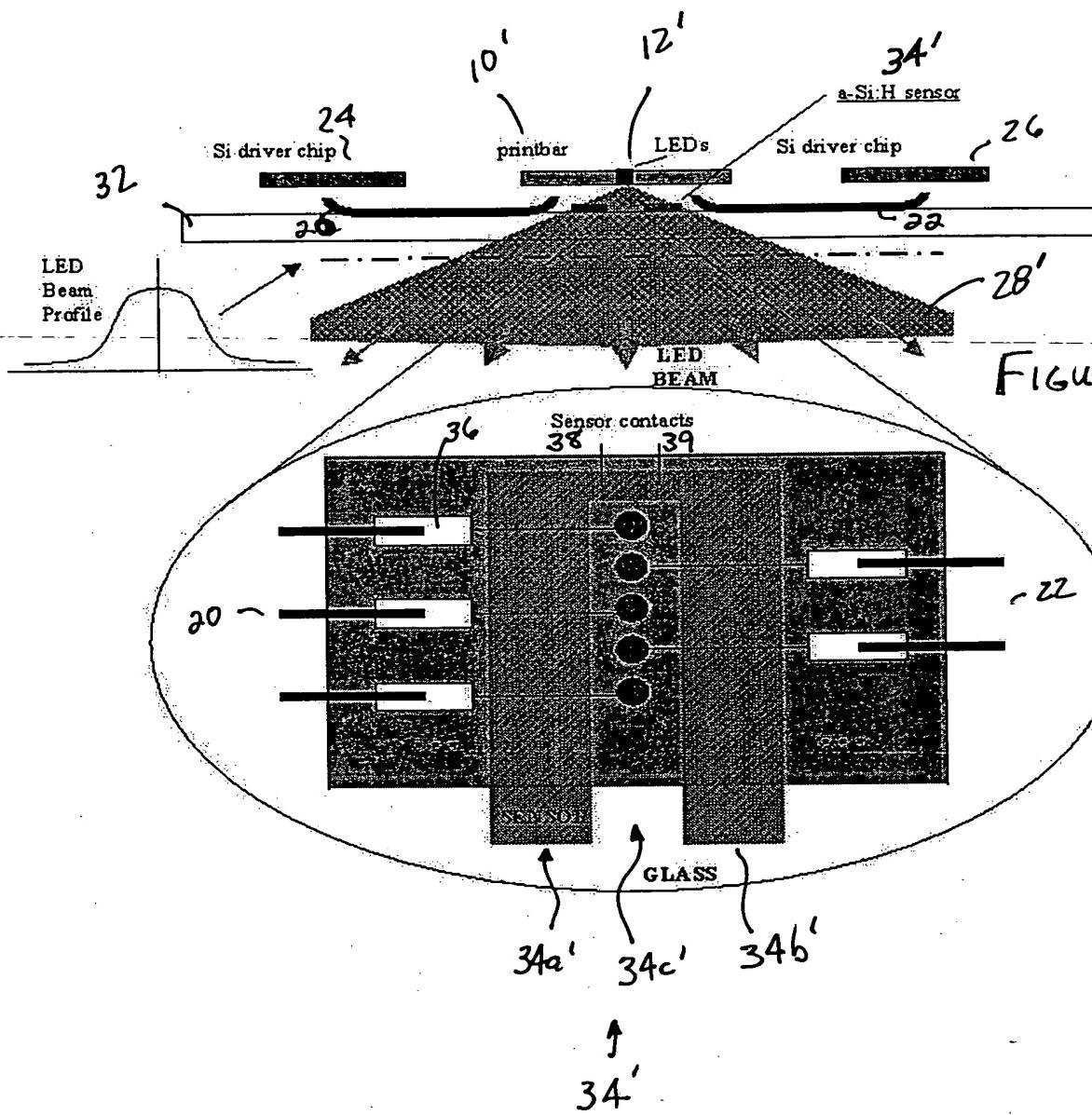


FIGURE 11

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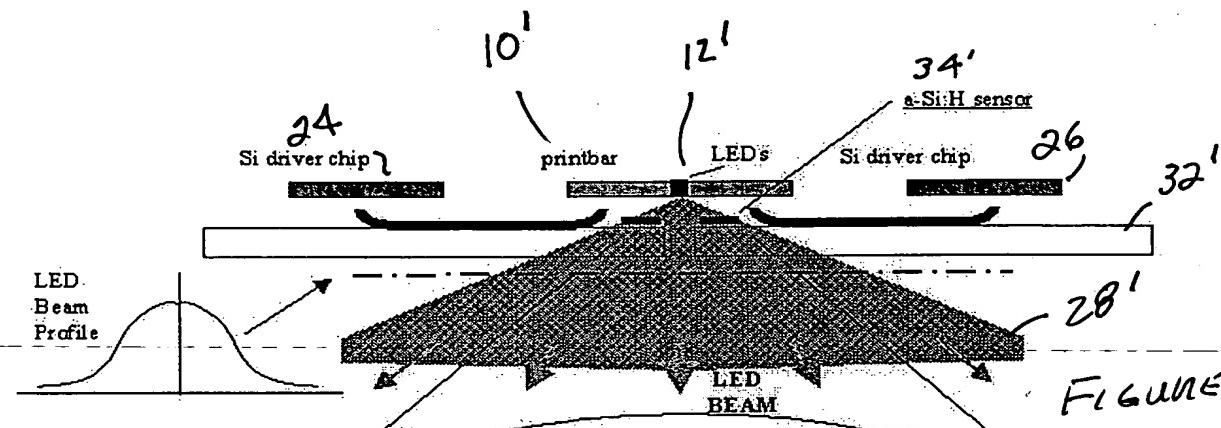


FIGURE 12

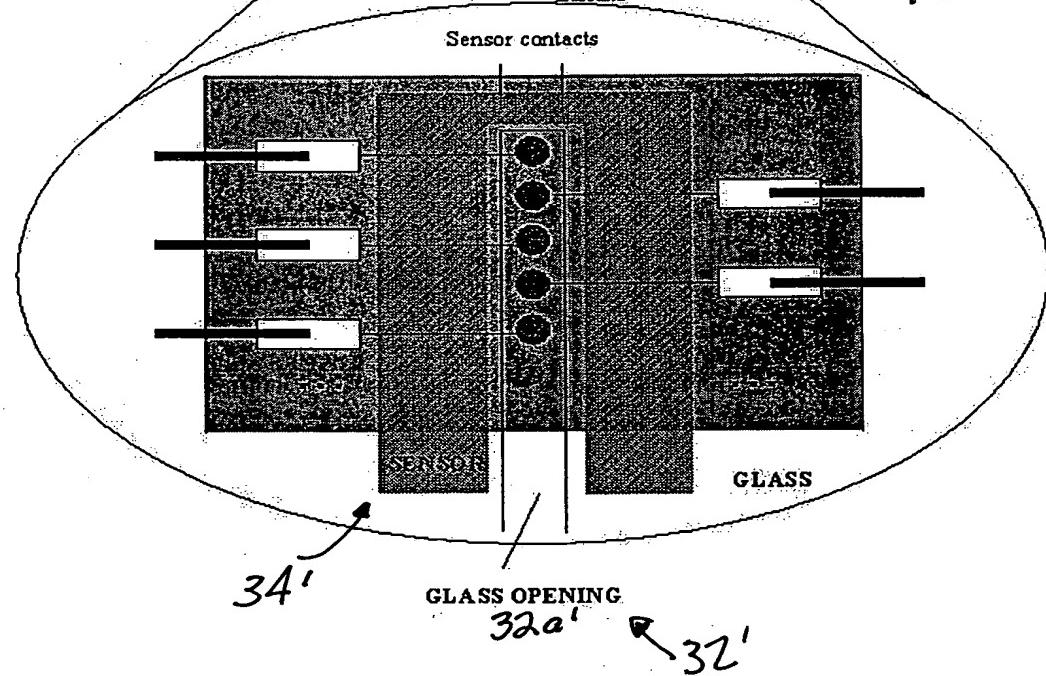


FIGURE 13

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24(26)
Si Driver Chip

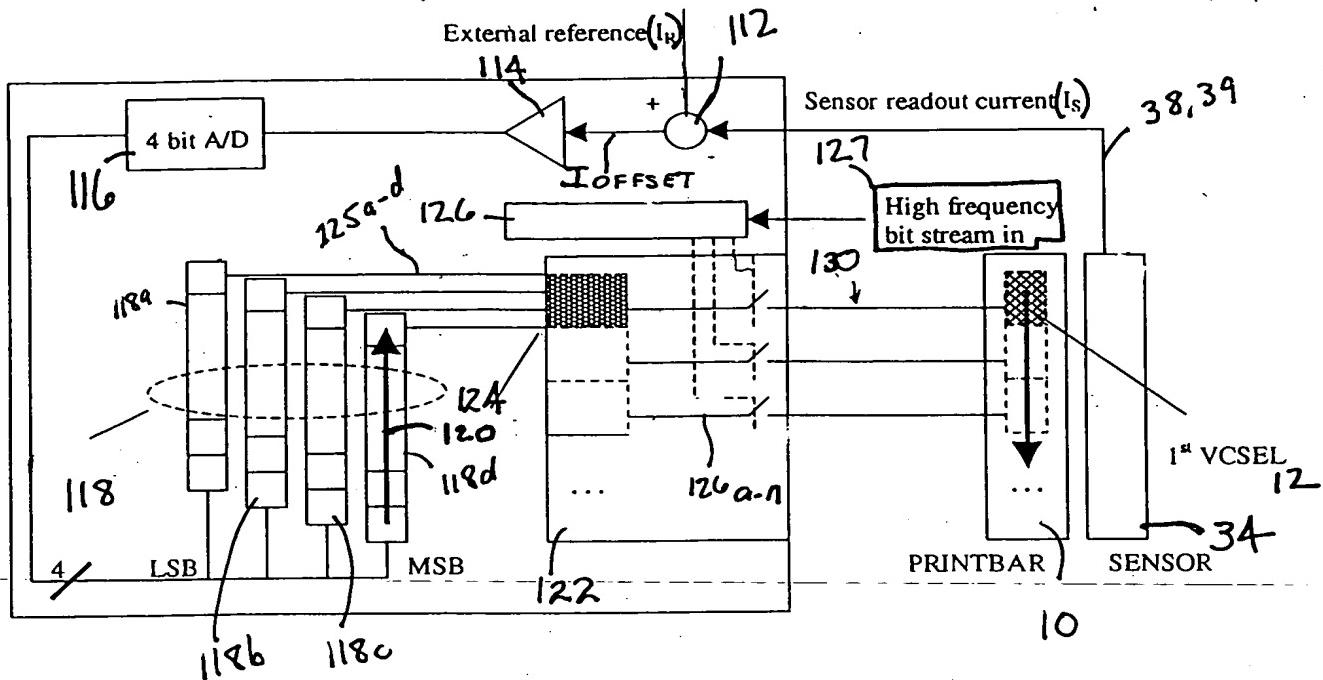


FIGURE 14

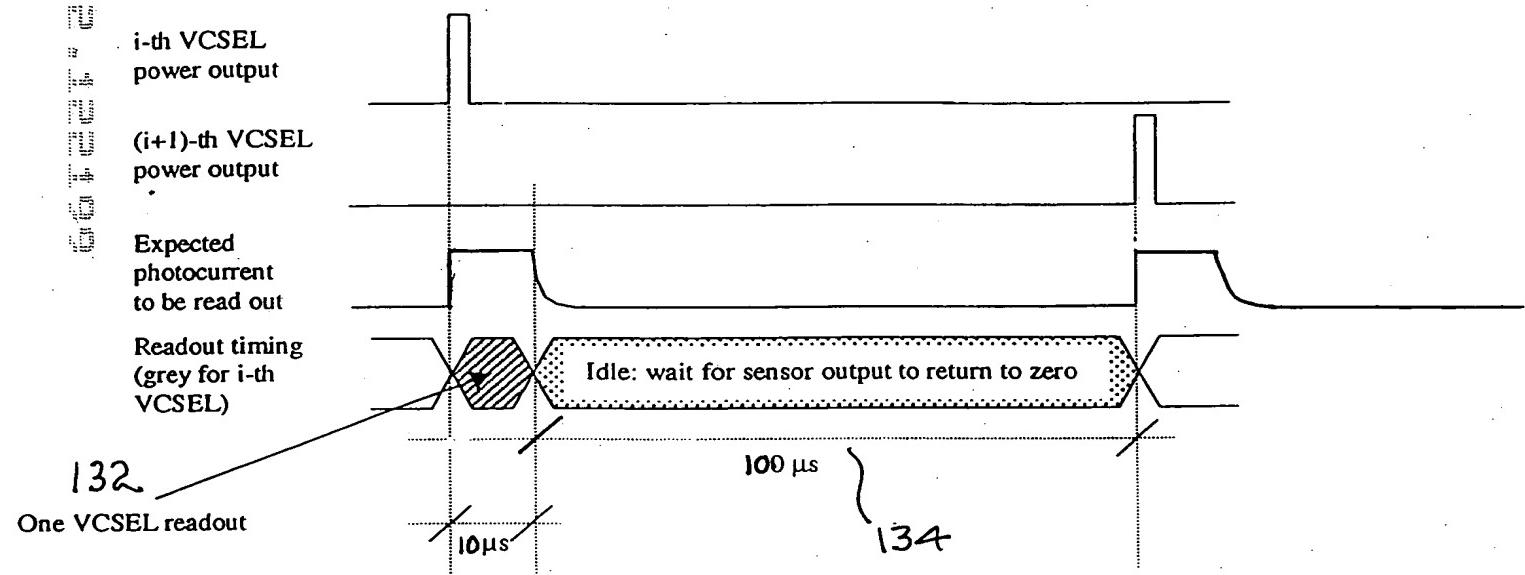


FIGURE 15

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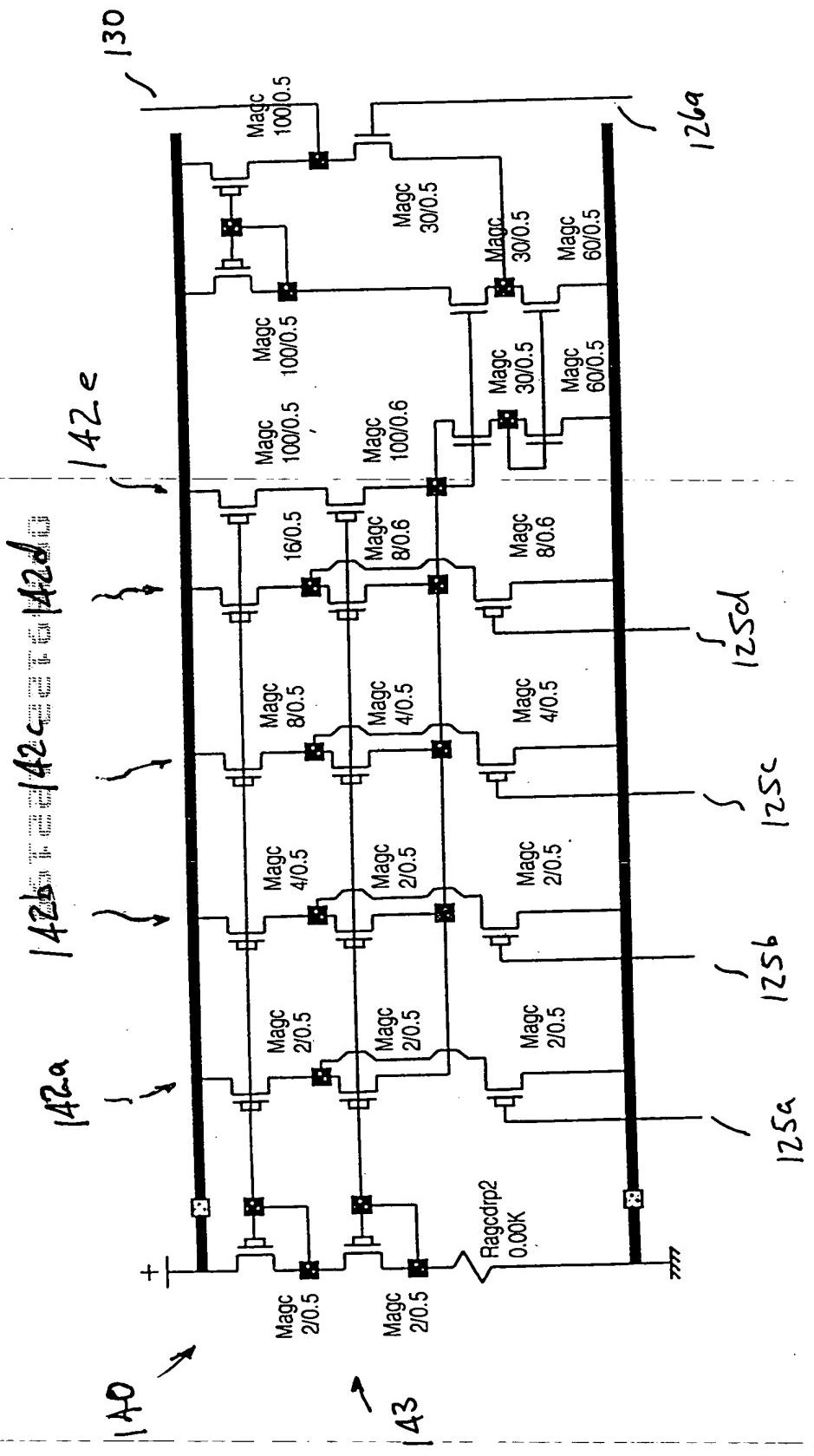


FIGURE 16